Erçetin Kuyucu

Personal Information

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Education

Middle East Technical University (METU)
Bachelor of Electrical and Electronics Engineering (2014-2020: Expected)
Cumulative GPA: 3.09/4 *Term GPA: 3.72/4*

Relevant Course Work

- Computer Architecture I-II

  In this course the top-down digital system design procedure using algorithmic state machine approach and the basis of basic computer architecture in register and instruction execution levels learned. ARM structure is observed in the form of registers controllers and data path. Single cycle, multicycle and the pipelined model of the ARM structure is observed. Also, some example models examined.

- Engineering Design I-II

  Basically, the capstone project that is developed by the team of students. The details of projects are provided at the Capstone Project website of METU. Each project has a special topic based on image processing efficient use of sensors and the management of a real time project.

- Introduction to Computer Networks

  The layered network model is observed. Queue, buffer and the probabilistic calculations of a computer network is also a part of this course.

- Interdisciplinary Design Studio

  Interdisciplinary collaboration in design, design thinking methods including brainstorming techniques, user research and problem definition, prototyping and user testing, project management considering economic and technological constraints, university-industry collaboration for developing innovative design projects.

- Analog Electronics

  The applications and design of basic single-stage transistor amplifiers and frequency responses, multi-stage amplifiers, feedback in amplifiers, differential pair stages, current mirrors and operational amplifiers are learned.

- Digital Electronics

  Design of the large signal transistor models, ttl-mos-cmos logic gates, multivibrators, RAM-ROM, A/D-D/A are learned.

- Logic Design

  In the course binary systems and boolean algebra, boolean function simplification, combinational logic, sequential synchronous logic, registers and counters are observed. Moreover, applications on the FPGA done using Verilog HDL. An oscilloscope is designed using an Altera FPGA development board as a project of course.

Isparta Süleyman Demirel Science High School (2010-2014)
Graduation Point: 95/100 University Exam Ranking: 1042 / 2 Million

Relevant Course Work

- Mathematic Competition Team Member
- Science Fair Organization Team Member

Skills

- Quartus II 13.1, Quartus Prime Lite 18.0 (Intermediate)
- Model-Sim (Intermediate)
- C++, C (Good), Python (Beginner)
- PCBWeb (Intermediate), Altium (Beginner), LT Spice (Advanced)
- MATLAB (Intermediate)
- IAR (Intermediate)
- MSP430 (Intermediate), Altera FPGA (Intermediate)

Experience

(July'17 – Aug'17)

I was working in the design of a FPGA board in hardware form. Boards objective is to test the inputs and the outputs of the boards that produced in the company. Board is called as "Test Board". In hardware basis I was responsible for the search of the IC's that fulfils the demands of company, designing a PCB that IC's are integrated with the Spartan-6 FPGA. Peripheral IC's that uses the TTL, RS485, A/D and D/A signals are used. Finally, board is produced then every component is mounted.

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(July'18 Aug'18)

I was responsible for the development of the current modules that are produced in the company. The modules use PIC-MSP430 based controllers that programmed through IAR software. Programming is done in register level. During internship using a PIC controller a RF module is programmed for communicating in the certain frequency, a board produced for MESAN Lock is reconfigured for operation in the 5V instead of 12V. Additionally, for implementation of GNSS modules to current projects, Real Time Kinematics and the RTCM3 protocol are searched. Instead of using a module that has these features, a controller is used to facilitate these functions.

Extracurricular Activities

From preparatory year until the end of third year of my bachelor I was a part of IEEE METU. In first the year I was a member of Power and Energy Society during that interval I was involved in the preparation of a Tesla coil. In the second year I also attended Robotics and Automation Society, BILTEK magazine crew and The Promotion and Advertisement Committee. I learned a lot of things including programming, coding, advertisement, preparing and hosting an event which are The First Power and Energy Congress of METU and The Campus Development Days which is the traditional event of IEEE METU. In next year I was the head of advertisement committee. I tried my best to advertise the events and the activities of IEEE METU. Also participated to every event as a head of advertisement team. In last year I was in The Steering Committee of IEEE METU. I was responsible for the external and the internal affairs. As a steering committee we organized a Hackathon, The Campus Development Days, METU Power and Energy Congress and the International Women in Engineering Day which foreign students are welcomed without hesitating about the language. These were enjoyable and developing years for me. In terms of organization planning and the human relations it was very beneficial.

In the end of preparatory year, I attended to the Work and Travel program. I worked in USA for 3 months. I was working for The Cherie's Bicycle and Blade Rental. I was responsible for the rental of bicycles. I worked at Virginia State which was at the east coast of United States. It was a great opportunity to discover new cultures, life styles, working conditions.

References

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